

**SUPPLEMENTAL  
Notice of Allowability**

**Application No.**

10/690,324

**Examiner**

MARK A. MAIS

**Applicant(s)**

LEE, JI YOUNG

**Art Unit**

2467

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to Interview on 18 March 2010.
2. ☒ The allowed claim(s) is/are 1-4, 6-54, and 56-72 [renumbered 1-70].
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☒ All    b) ☐ Some\*    c) ☐ None    of the:
    1. ☒ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
  - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
    - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
  - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.

**Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).**
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

- |   |   |
|---|---|
| <ol style="list-style-type: none"> <li>1. <input type="checkbox"/> Notice of References Cited (PTO-892)</li> <li>2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)</li> <li>3. <input type="checkbox"/> Information Disclosure Statements (PTO/SB/08),<br/>Paper No./Mail Date _____</li> <li>4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit<br/>of Biological Material</li> </ol> | <ol style="list-style-type: none"> <li>5. <input type="checkbox"/> Notice of Informal Patent Application</li> <li>6. <input checked="" type="checkbox"/> Interview Summary (PTO-413),<br/>Paper No./Mail Date _____.</li> <li>7. <input checked="" type="checkbox"/> Examiner's Amendment/Comment</li> <li>8. <input type="checkbox"/> Examiner's Statement of Reasons for Allowance</li> <li>9. <input type="checkbox"/> Other _____.</li> </ol> |
|---|---|

/MARK A. MAIS/  
Examiner, Art Unit 2467

/Pankaj Kumar/  
Supervisory Patent Examiner, Art Unit 2467

### **EXAMINER'S AMENDMENT**

1. During a telephone conversation conducted on March 3, 2010, Leah Dewar authorized the following examiner's amendment. This Examiner's Amendment, which places this application in condition for allowance, requires no extension of time under MPEP 706.07(f)(F) because Applicants' Amendment after Final was filed on January 25, 2010—within two months of the Final Rejection mailed on November 24, 2009.
2. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

### ***Information Disclosure Statement***

3. The information disclosure statement filed on January 25, 2010 does not fully comply with the requirements of 37 CFR 1.98(a)(3) because: (a) it does not include a concise explanation of relevance, as it is presently understood by the individual designated in 37 CFR 1.56(c) most knowledgeable about the content of the information [under 37 CFR 1.98 (a)(3)(i)] or (b) a copy of the English-language translation, or portion thereof, of the Japanese Patent Office's Office Action dated December 1, 2009 for the corresponding Japanese Application number 2004-009706 [under 37 CFR 1.98 (a)(3)(ii)]. It has been placed in the application file, but the non-complying information referred to therein has not been considered. Since the submission

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appears to be *bona fide*, applicant is given **ONE (1) MONTH** from the date of this notice to supply the above mentioned omissions or corrections in the information disclosure statement.

NO EXTENSION OF THIS TIME LIMIT MAY BE GRANTED UNDER EITHER 37

CFR 1.136(a) OR (b). Failure to timely comply with this notice will result in the above mentioned information disclosure statement being placed in the application file with the noncomplying information **not** being considered. See 37 CFR 1.97(i).

### *Claims*

4. Please amend the following claims:

Claim 1. (Currently Amended) A system for transferring a signal to a channel, comprising:

a storage unit dedicated to the channel for storing source identification information of a plurality of predetermined sources, the source identification information indicating an order of priority of the plurality of predetermined sources for access to the channel;

a plurality of selection circuits for receiving input signals from at least one of the plurality of predetermined sources and the source identification information of the plurality of predetermined sources from the storage unit, each of the selection circuits receives source identification information for one of the plurality of predetermined sources and each of the selection circuits selects ~~selecting~~ one of the plurality of input signals in response to the source identification information for the one of the plurality of predetermined sources wherein the

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plurality of selection circuits receive the source identification information for the plurality of predetermined sources in order of priority such that a first selection circuit of the plurality of selection circuits receives source identification information for a highest-priority source; and

a circuit for checking outputs of the selection circuits and forwarding selected input signals to the channel,

wherein the storage unit stores the source identification information in a long bit sequence in an order of priority and stores the source identification information for the a highest-priority source in the most significant bits of the storage unit.

Claim 23. (Currently Amended) A system for transferring signals to channels, comprising; :

a plurality of storage units, each storage unit being dedicated to one of the channels, and each storage unit being adapted to store source identification information indicating an order of priority of a plurality of predetermined sources for access to the channel;

for each of the plurality of channels, a plurality of selection circuits for receiving input signals from at least one of the plurality of predetermined sources and the source identification information of the plurality of predetermined sources from the plurality of storage units, each of the selection circuits receives source identification information for one of the plurality of predetermined sources and each of the selection circuits selects ~~selecting~~ one of the plurality of input signals in response to the source identification information for the one of the plurality of predetermined sources, wherein the plurality of selection circuits receive the source identification information for the plurality of predetermined sources in order of priority such that a first

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selection circuit of the plurality of selection circuits receives source identification information for a highest-priority source; and

for each of the plurality of channels, a circuit for checking outputs of the selection circuits and forwarding selected input signals to the channel,

wherein each storage unit stores the source identification information in a long bit sequence in an order of priority and stores the source identification information for the a highest-priority source in the most significant bits of the storage unit.

Claim 37. (Currently Amended) A direct memory access (DMA) controller for controlling transfer of signals from predetermined input sources to output devices, a plurality of channels being connected to the output devices, the DMA controller comprising:

a plurality of storage units, each storage unit being dedicated to one of the channels, and each storage unit being adapted to store source identification information indicating an order of priority of the predetermined sources for access to the channel;

for each of the plurality of channels, a plurality of selection circuits for receiving input signals from at least one of the predetermined sources and the source identification information of the predetermined sources from the plurality of storage units, each of the selection circuits receives source identification information for one of the plurality of predetermined sources and each of the selection circuits selects selecting one of the plurality of input in response to the source identification information for the one of the plurality of predetermined sources, wherein the plurality of selection circuits receive the source identification information for the plurality of

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predetermined sources in order of priority such that a first selection circuit of the plurality of selection circuits receives source identification information for a highest-priority source; and

for each of the plurality of channels, a circuit for checking outputs of the selection circuits and forwarding selected input signals to the channel,

wherein each storage unit stores the source identification information in a long bit sequence in an order of priority and stores the source identification information for the a highest-priority source in the most significant bits of the storage unit.

Claim 51. (Currently Amended) A method for transferring signal to a channel, comprising:

storing source identification information for a plurality of predetermined sources in a storage unit, the source identification information indicating an order of priority of the plurality of predetermined sources for access to the channel;

providing a plurality of selection circuits for receiving input signals from at least one of the plurality of predetermined sources and the source identification information of the plurality of predetermined sources, each of the selection circuits receiving source identification information for one of the plurality of predetermined sources and each of the selection circuits selecting one of the plurality of input signals in response to the source identification information for the one of the plurality of predetermined sources, wherein the plurality of selection circuits receive the source identification information for the plurality of predetermined sources in order of priority such that a first selection circuit of the plurality of selection circuits receives source identification information for a highest-priority source;

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with a checking circuit, checking outputs of the selection circuits and forwarding a selected input signal to the channel,

wherein the storage unit stores the source identification information in a long bit sequence in an order of priority and stores the source identification information for the a highest-priority source in the most significant bits of the storage unit.

***Allowable Subject Matter***

5. Claims 1-4, 6-54, and 56-72 [renumbered 1-70] are allowed.

***Conclusion***

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to MARK A. MAIS whose telephone number is (571)272-3138. The examiner can normally be reached on 5am-4pm.

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7. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Pankaj Kumar can be reached on 571-272-3011. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

8. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

March 18, 2010

/MARK A. MAIS/

Examiner, Art Unit 2467

/Pankaj Kumar/

Supervisory Patent Examiner, Art Unit 2467